

## PATENT APPLICATION

Group Art Unit: 2829

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Akihiko EBINA et al.

S. Geyer Examiner: Docket No.: 111778

Application No.: 10/052,255

METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR MEMORY DEVICES Filed: January 23, 2002 For:

## AMENDMENT IN RESPONSE TO QUAYLE ACTION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In reply to the August 13, 2003 Quayle Action, please consider the following:

Amendments to the Specification;

Amendments to the Claims as reflected in the listing of claims;

Amendments to the Drawings include an attached replacement sheet; and

Remarks.

TECHNOLOGY CENTER 2800